

1    ABSTRACT OF THE DISCLOSURE:

2            A control circuit for use in a video processor utilizes  
3       combined automatic kinescope bias (AKB) control, and average  
4       individual beam current sensing and limiting in at least one CRT.  
5       The control circuit includes automatic kinescope bias (AKB) control  
6       circuitry for detecting a magnitude of individual red (R), green  
7       (G) and blue (B) cathode currents driving corresponding R, G and B  
8       CRTs, generating R, G and B average cathode current control signals  
9       therefrom, and using the R, G and B average cathode current control  
10      signals as feedback to the video processor to reduce the R, G and B  
11      cathode currents approximately equal current amounts. Selective  
12      beam current limiting circuitry within the control circuitry  
13      compares at least one of the R, G and B average current control  
14      signals with a predetermined signal, and whereupon the at least one  
15      of the R, G and B average current control signals exceeds the  
16      predetermined signal, introducing a gain reduction in corresponding  
17      video gain stages within the video processor to limit the at least  
18      one of the R, G and B average current control signals.